

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Accurate Generation of Scan Enable Signal when Testing Integrated Circuits Using Sequential Scanning Techniques							
Application Number :								
Date :								
First Named Applicant:	Mr. Hasanuzzaman K SHEIKH							
Confirmation Number:								
Attorney Docket Number:	TI-36330							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Mr. Narendra Reddy Thappeta Registered Number: 41416</td><td>(Naren Thappeta)</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Mr. Narendra Reddy Thappeta Registered Number: 41416	(Naren Thappeta)	Attorney
Submitted By:	Elec. Sign.	Sign. Capacity						
Mr. Narendra Reddy Thappeta Registered Number: 41416	(Naren Thappeta)	Attorney						

Documents being submitted:	Files
us-fee-sheet	txnefiling-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	txnefiling-usidst.xml us-ids.dtd us-ids.xsl
us-request	txnefiling-usrequ.xml us-request.dtd us-request.xsl
application-body	Appltxn72-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd figtxn72_1.tif figtxn72_2.tif figtxn72_3.tif figtxn72_4.tif figtxn72_5.tif figtxn72_6.tif figtxn72_7.tif figtxn72_8.tif
us-declaration	decltxn72pg1.tif
us-declaration	decltxn72HSpG.tif
Comments	